

Notice of References Cited	Application/Control No. 10/773,244	Applicant(s)/Patent Under Reexamination AOSHIMA, TOMOYASU	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,956,600	09-1999	Kuroi et al.	438/446
*	B	US-5,738,575	04-1998	Bock, Robert T.	433/216
	C	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 114-115, 529, 531-532.
	V	Streetman, Solid State Electronic Devices, 1990, Prentice Hall, 3rd ed., p. 332.
	W	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.